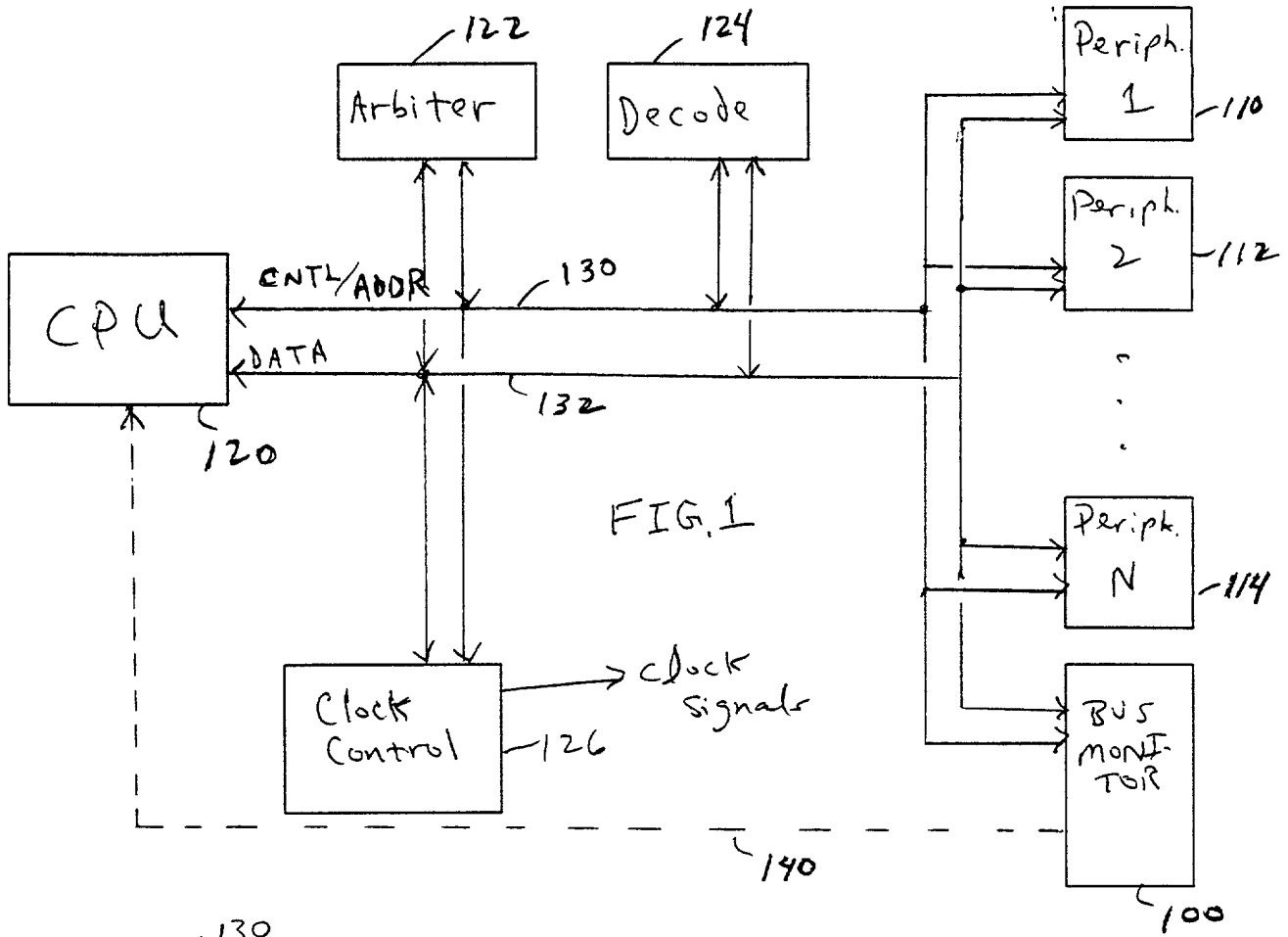
122
Arbiter
124
Decode
Periph. 1
110
Periph. 2
112
CPU
CNTL/ADDR
130
DATA
132
120
Periph. N
114
Clock Control
126
clock signals
BUS MONI-TOR
140
100

FIG. 1

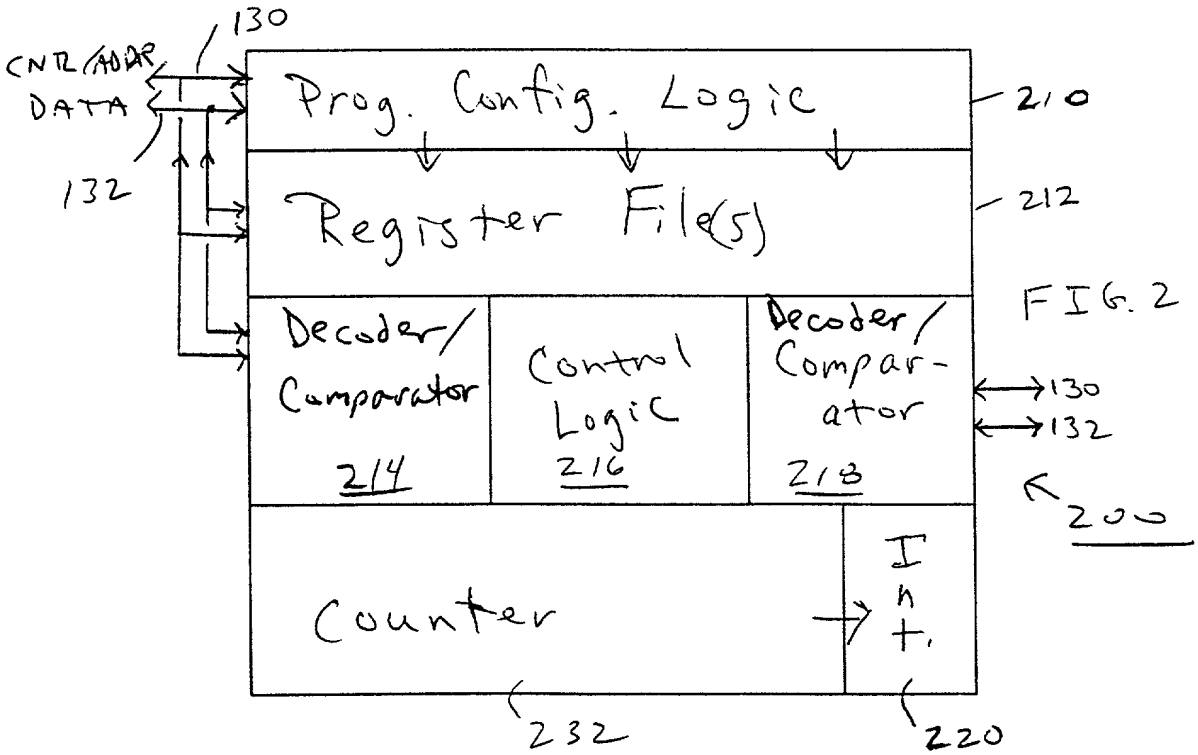
130
CNTL/ADDR
DATA
132
Prog. Config. Logic
210
Register File(s)
212
Decoder/ Comparator
214
Control Logic
216
Decoder/ Compar-ator
218
130
132
200
Counter
Int.
232
220

FIG. 2